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XA-9590

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Masao SHINOZAKI et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE

* * *

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified
patent application as indicated below.

IN THE CLAIMS:

Please amend Claims 3, 4 and 7 as set forth below.

- 1 3. (Amended) A semiconductor device according to Claim
2 1, wherein a power supply voltage applied to the first MOS
3 transistors constituting the input circuit or the output